

**Amendments to the Abstract**

Please **amend** the Abstract to read.

-- A timing generator and method of developing drive signals ~~(S10)~~ which, when input sync signals ~~(S10)~~ are present, delivers the leading and trailing edges of the input pulses ~~(F<sub>s</sub>)~~ directly to a drive output ~~(F<sub>d</sub>)~~ and which generates a free-running signal ~~(S20')~~ when input sync pulses are not present.--